

- [54] **ELECTRONIC POSTAGE METER HAVING A MEMORY MAP DECODER**
- [75] **Inventor:** Peter C. DiGiulio, Easton, Conn.
- [73] **Assignee:** Pitney Bowes Inc., Stamford, Conn.
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- [58] **Field of Search** ..... 364/200, 900; 371/67; 235/419

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*Primary Examiner*—Raulfe B. Zache  
*Assistant Examiner*—Emily Y. Chan  
*Attorney, Agent, or Firm*—Michael J. DeSha; David E. Pitchenik; Melvin J. Scolnick

[57] **ABSTRACT**

An electronic postage meter has an improved memory selection circuit wherein custom memory map decoder circuit with resolution down to a single byte location is used to provide selection enabling signals to insure the selection of an appropriate device only when the addresses appropriate to that device are communicated. In accordance with the invention, at least two nonvolatile memories are provided. Writing to either of these nonvolatile memories is inhibited unless one and only one memory is selected. The circuit also prevents the selection of either of the nonvolatile memories in the event that the write strobe signal to the memories is held active.

[56] **References Cited**

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**3 Claims, 15 Drawing Sheets**

